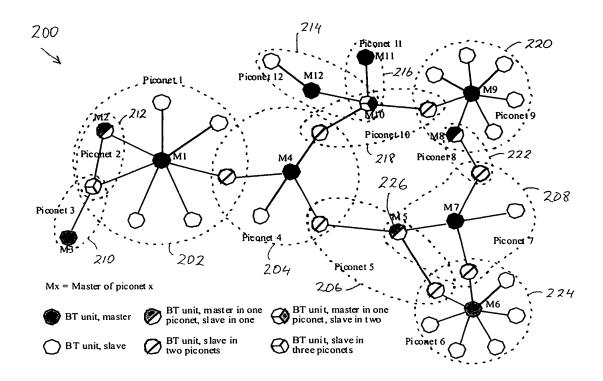
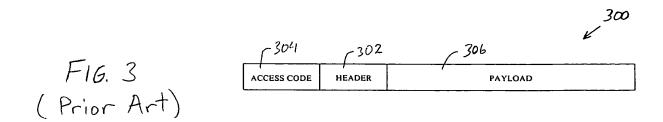
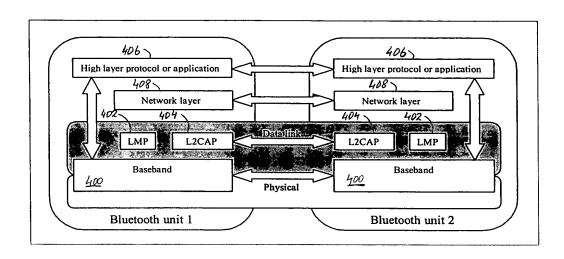


FIG. 1 (Prior Art)

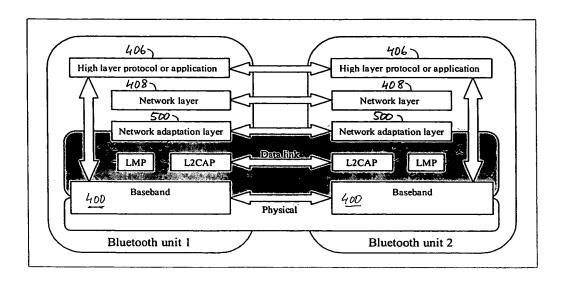


F16. 2 (Prior Art)





F16.4 (Prior Art)



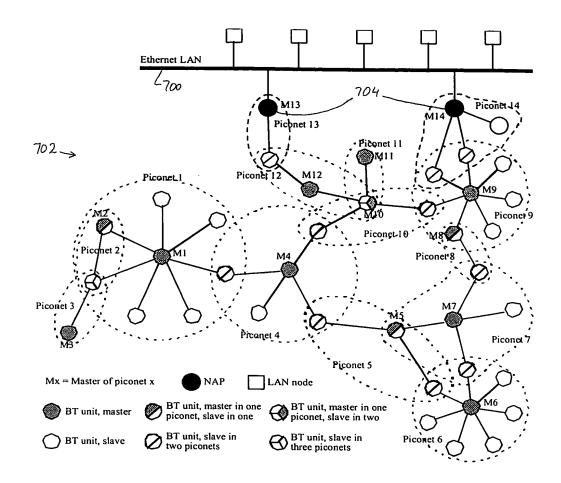
F16. 5 (Prior Art)

PARITY BITS LAP BUS UAP NAP Class of device ADDR CLK scan mode

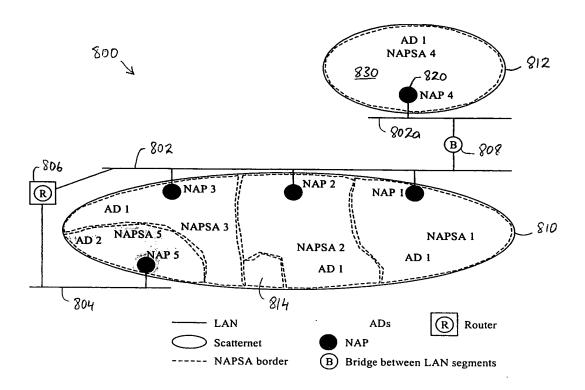
602 604 606 612 614 616 618 620 622

F1G. 6 (Prior Art)

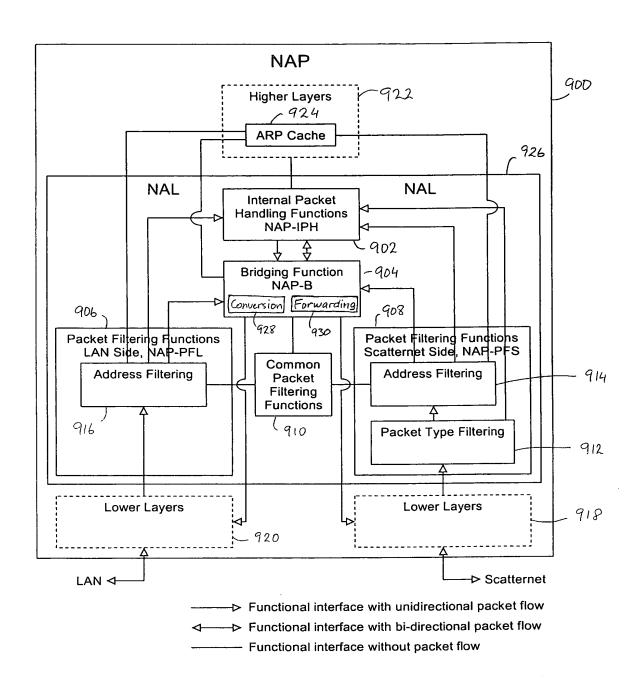
•



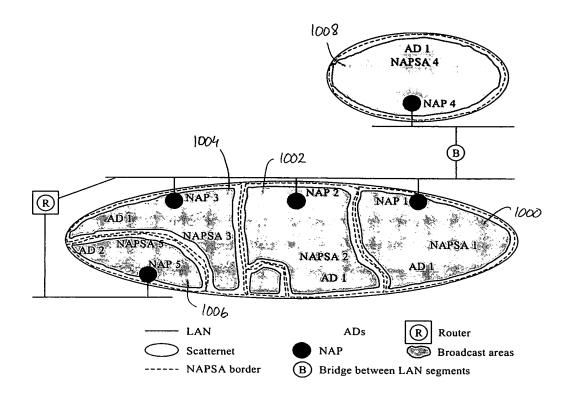
F16. 7 (Prior Art)



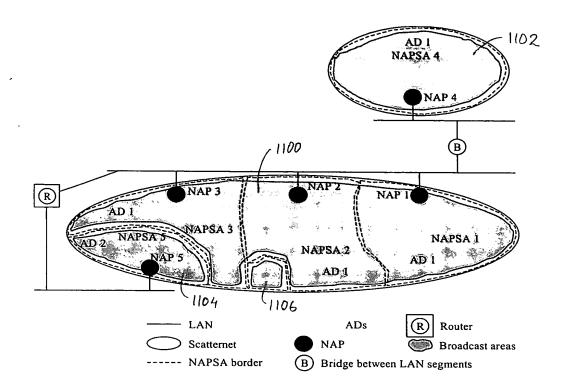
F16. 8 (Prior Art)



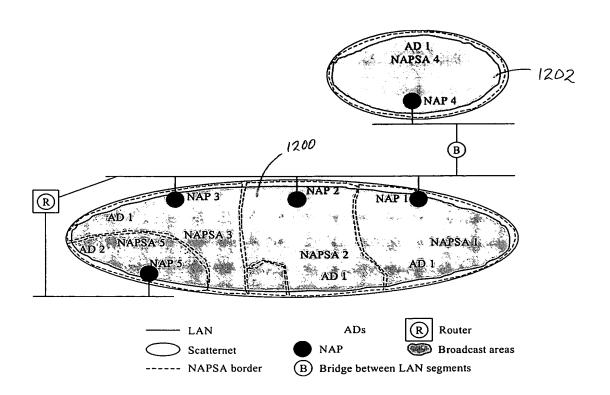
F16. 9



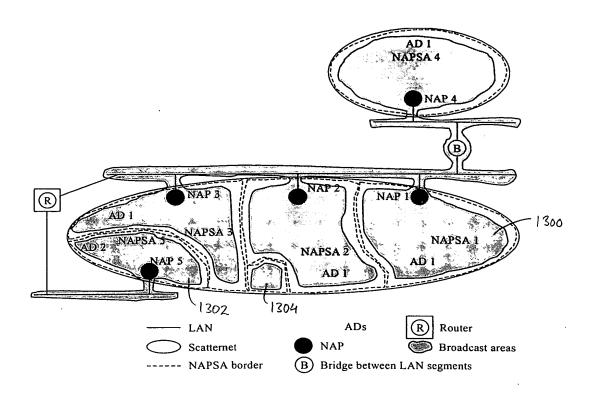
F16. 10



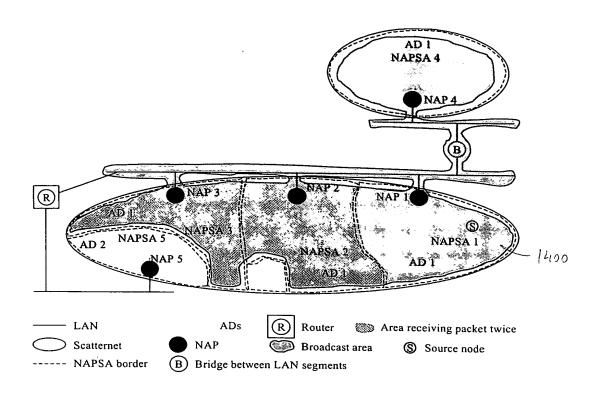
F16. 11



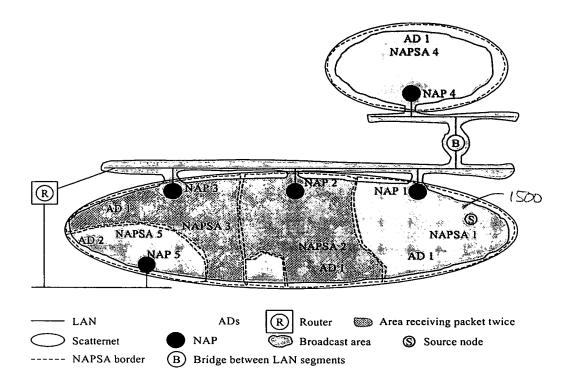
F16. 12



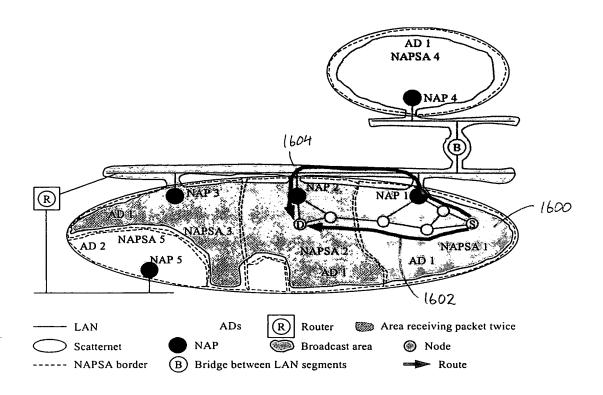
F16. 13



F16.14



F16. 15

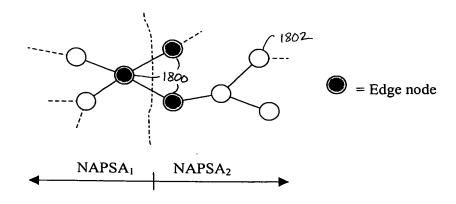


F16. 16

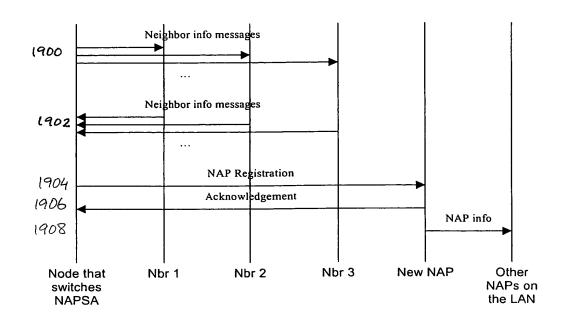
Ethernet packet format

	6	6	_ 2	46 - 1500	4
1700 ->	Destination address	Source address	Туре	Data	CRC
	1702	1704	1706	1708	
NAL unicast packet format					
7 bits	l bio 6	6	2	0 - 1500	
1710 → NAL type	E Destination address	Source address	Туре	Data	
1712 17	ノ 4				
NAL broadcast packet format					
7 bits 1 bit 2 bits 4	6	6	2	0 - 1500	į
1716 -> NAL E B-cast Broad type seq. n		Source address	Туре	Data	
) /					
1718 17:	20				

F16. 17



F16. 18



F16. 19

F16. 21